Sir:

Transmitted herewith for filing is the Patent Application of:

Ator: YEEN TAT CHAN, KHENG CHOK TEE, YIANG AUN NGA, ZHAO LUN, WANG LING GOH, DIING SHENP ANG

## METHOD OF FORMING A PARTIALLY DEPLETED SILICON ON INSULATOR (PDSOI) TRANSISTOR WITH A PAD

P TO	LOCK BOD	Y EXTENSION					
Enclosed	i are:						
X	5 sheets of drawing(s) - formal.						
x	An assignment of the invention to Chartered Semiconductor Manufacturing Ltd.						
	An associate power of attorney Applicant claims small entity status						
	Request & Certification under 35 USC 122(b)(2)(b)(i)						
The filing fee has been calculated as shown below:							
		(Col. 1)	(Col. 2)	(Col. 2) OTHER THAN			
FOR:		NO. FILED	•NO. EXTRA	RATE	FEE		
BASIC FEE		> <			\$ 750.		
TOTAL CLAIMS		44 -20=	24	x 18 =	\$ 432.		
INDEP CLAIMS		4 -3=	1	x 84 =	\$ 84.		
	CLAIMS				\$ 1,266.		
				SUB TOTAL			
				ASSIGNMENT	\$40.		
				TOTAL	\$ 1.306.		
X	Please charge my Deposit Account No. 19-0033 in the amount of \$ 1,306. A duplicate copy of this sheet is enclosed.						
X	The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any						
overpayment to Deposit Account No. 19-0033. A duplicate copy of this sheet is enclosed.  X Any additional filing fees required under 37 CFR §1.16.							
	•	Respectfully submitted,					
STAPHEN S. ACKERMAN, REG. NO. 37,761							

## EXPRESS MAIL CERTIFICATE

## Express Mail No.EV313928154US

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to Assistant Commissioner of Patents, Alexandria, VA, 22313-1450. Applicant and/or Attorney requests the date of deposit as the Filing Date.